

Fundamental Algorithms

Chapter 6: Parallel Algorithms – The PRAM Model

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Example: Parallel Searching

Definition (Search Problem)

Input: a set A of n elements $\in \mathcal{A}$, and an element $x \in \mathcal{A}$.

Output: The (smallest) index $i \in \{1, \dots, n\}$ with $x = A[i]$.

An immediate solution:

- use n processors
- on each processor: compare x with $A[i]$
- return matching index/indices i

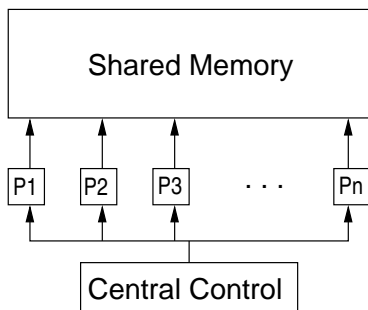
Simple Parallel Searching

```
ParSearch(A: Array[1..n], x: Element) : Integer {  
  for i from 1 to n do in parallel {  
    if x = A[i] then return i;  
  }  
}
```

Discussion:

- Can all n processors access x simultaneously?
→ **exclusive** or **concurrent** read
- What happens if more than one processor finds an x ?
→ **exclusive** or **concurrent** write (of multiple returns)

The PRAM Models

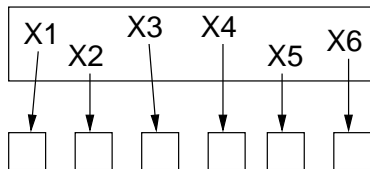


Concurrent or Exclusive Read/Write Access:

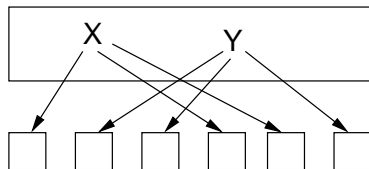
- EREW** exclusive read, exclusive write
- CREW** concurrent read, exclusive write
- ERCW** exclusive read, concurrent write
- CRCW** concurrent read, concurrent write

Exclusive/Concurrent Read and Write Access

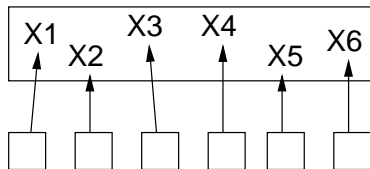
exclusive read



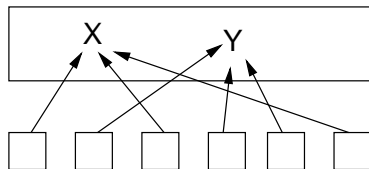
concurrent read



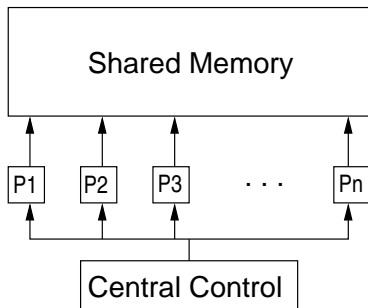
exclusive write



concurrent write



The PRAM Models (2)



SIMD

- Underlying principle for parallel hardware architecture: strict single instruction, multiple data (SIMD)
- ⇒ All parallel instructions of a parallelized loop are performed synchronously (applies even to simple if-statements)

Parallel Search on an EREW PRAM

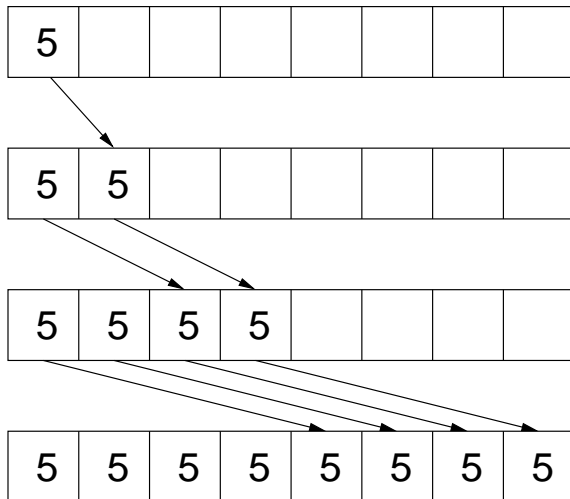
ToDo's for exclusive read and exclusive write:

- avoid exclusive access to x
⇒ replicate x for all processors (“broadcast”)
- determine smallest index of all elements found:
⇒ determine minimum in parallel

Broadcast on the PRAM:

- copy x into all elements of an array $X[1..n]$
- note: each processor can only produce one copy per step

Broadcast on the PRAM – Copy Scheme



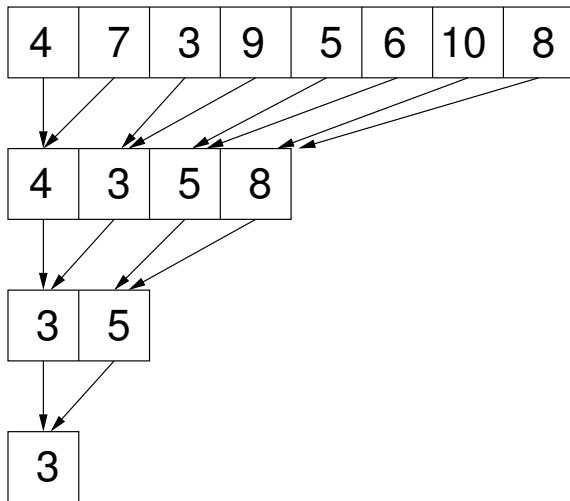
Broadcast on the PRAM – Implementation

```
BroadcastPRAM( x:Element, A:Array[1..n]) {  
    // n assumed to be 2^k  
    // Model: EREW PRAM  
  
    A[1] := x;  
    for i from 0 to k-1 do  
        for j from 2^i+1 to 2^(i+1) do in parallel {  
            A[j] := A[j-2^i];  
        }  
    }  
}
```

Complexity:

- $T(n) = \Theta(\log n)$ on $\frac{n}{2}$ processors

Minimum Search on the PRAM – “Binary Fan-In”



Minimum on the PRAM – Implementation

```

MinimumPRAM( A: Array [1..n] ) : Integer {
    // n assumed to be 2^k
    // Model: EREW PRAM

    for i from 1 to k do {
        for j from 1 to n/(2^i) do in parallel
            if A[2j-1] > A[2j]
            then A[j] := A[2j];
            else A[j] := A[2j-1];
            end if;
        }
    return A[1];
}

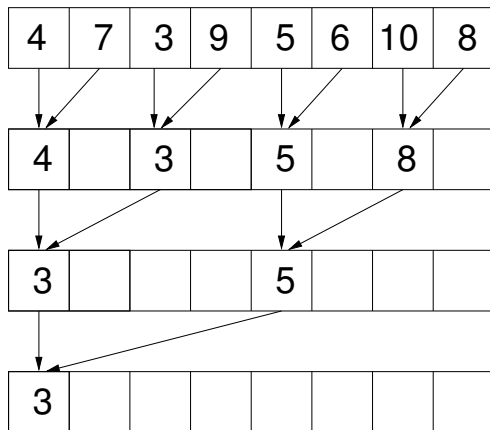
```

Complexity: $T(n) = \Theta(\log n)$ on $\frac{n}{2}$ processors

“Binary Fan-In” (2)

Comment Concerned about synchronous if-statement (guaranteed by SIMD assumptions)?

⇒ Modify stride!



Searching on the PRAM – Parallel Implementation

```
SearchPRAM( A:Array[1..n], x:Element) : Integer {  
    // n assumed to be 2^k  
    // Model: EREW PRAM  
  
    BroadcastPRAM(x, X[1..n]);  
  
    for i from 1 to n do in parallel {  
        if A[i] = X[i]  
        then X[i] := i;  
        else X[i] := n+1; // (invalid index)  
        end if;  
    }  
  
    return MinimumPRAM(X[1..n]);  
}
```

The Prefix Problem

Definition (Prefix Problem)

Input: an array A of n elements a_j .

Output: All terms $a_1 \times a_2 \times \dots \times a_k$ for $k = 1, \dots, n$.

\times may be any associative operation.

Straightforward serial implementation:

```
Prefix( A:Array[1..n]) {  
    // in-place computation:  
    for i from 2 to n do {  
        A[i] := A[i-1]*A[i];  
    }  
}
```

The Prefix Problem – Divide and Conquer

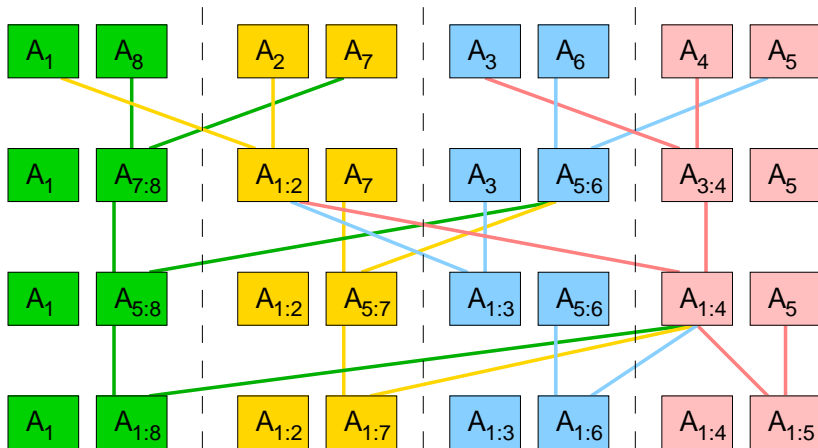
Idea:

1. compute prefix problem for $A_1, \dots, A_{n/2}$
→ gives $A_{1:1}, \dots, A_{1:n/2}$
2. compute prefix problem for $A_{n/2+1}, \dots, A_n$
→ gives $A_{n/2+1:n/2+1}, \dots, A_{n/2+1:n}$
3. multiply $A_{n/2}$ with $A_{n/2+1:n/2+1}, \dots, A_{n/2+1:n}$
→ gives $A_{1:n/2+1}, \dots, A_{1:n}$

Parallelism:

- steps 1 and 2 can be computed in parallel (divide)
- all multiplications in step 3 can be computed in parallel
- recursive extension leads to parallel prefix scheme

Parallel Prefix Scheme on a CREW PRAM



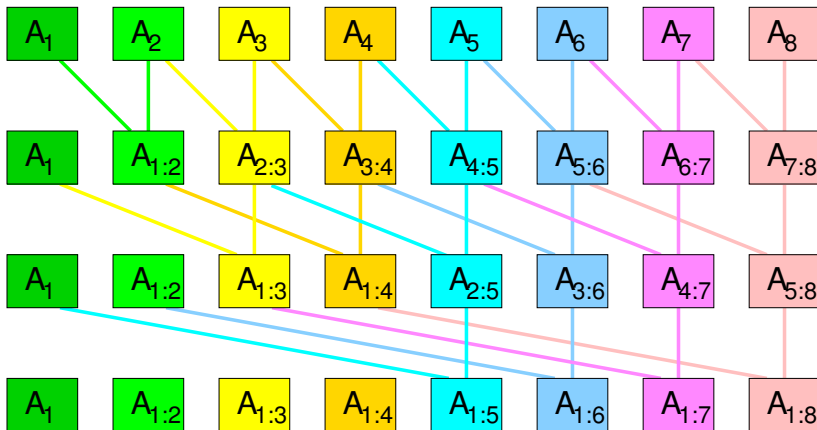
Parallel Prefix – CREW PRAM Implementation

```
PrefixPRAM( A:Array[1..n]) {  
  // n assumed to be 2^k  
  // Model: CREW PRAM (n/2 processors)  
  
  for l from 0 to k-1 do  
    for p from 2^l by 2^(l+1) to n do in parallel  
      for j from 1 to 2^l do in parallel {  
        A[p+j] := A[p]*A[p+j];  
      }  
    }  
}
```

Comments:

- p- and j-loop together: $n/2$ multiplications per l-loop
- concurrent read access to $A[p]$ in the innermost loop

Parallel Prefix Scheme on an EREW PRAM



Parallel Prefix – EREW PRAM Implementation

```
PrefixPRAM( A: Array[1..n]) {  
  // n assumed to be  $2^k$   
  // Model: EREW PRAM (n-1 processors)  
  
  for l from 0 to k-1 do  
    for j from  $2^{l+1}$  to n do in parallel {  
      tmp[j] := A[j- $2^l$ ];  
      A[j] := tmp[j]*A[j];  
    }  
}
```

Comment:

- all processors execute $\text{tmp}[j] := A[j-2^l]$ before multiplication!