

- p_i computes $a_i^{(k)}, b_i^{(k)}, c_i^{(k)}, d_i^{(k)} \rightarrow$ communication necessary for $a_j^{(k-1)}, b_j^{(k-1)}, c_j^{(k-1)}, d_j^{(k-1)},$
 $j \in \{i - 2^{k-1}, i, i + 2^{k-1}\}$. For $j = i - 2^{k-1}$ communication with $p_{i-2^{k-1}}$ necessary,
for $j = i + 2^{k-1}$ communication with $p_{i+2^{k-1}}$ necessary.
- per iteration every p_i is working / active
- After computation of step $k = N$, p_i will compute $x_i = \frac{d_i^{(N)}}{b_i^{(N)}}$
- Parallelization of H/G for case $n = pq$ (processor has block of rows)? Think about it!